

L Number	Hits	Search Text	DB	Time stamp
-	96	'digital circuit' and 'analog circuit' and 'buried layer'	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/05/06 09:45
-	811	257/372	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/09/25 13:44
-	173	257/516	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/06/24 08:47
-	226	257/535	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/06/21 14:03
-	1133	257/506	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/06/21 14:32
-	591	438/207	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/06/24 08:59
-	180	'conductive traces' and hole and net	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/21 15:40
-	1374	'conductive traces' and (257/\$.ccls. or 438/\$.ccls.)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/06/27 09:19
-	175	257/312	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/12/05 13:55
-	25	257/369 and (buried adj layer) and (NPN) and (PMOS) and (NMOS)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/12/05 15:03
-	122	257/370 and (buried adj layer) and (NPN) and (PMOS) and (NMOS)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/12/05 15:30
-	26	257/371 and (buried adj layer) and (NPN) and (PMOS) and (NMOS)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/12/05 15:34
-	32	257/372 and (buried adj layer) and (NPN) and (PMOS) and (NMOS)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/03 12:53
-	11	257/373 and (buried adj layer) and (NPN) and (PMOS) and (NMOS)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/12/05 15:41

-	12	257/374 and (buried adj layer) and (NPN) and (PMOS) and (NMOS)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/12/05 17:53
-	54	438/202 and (buried adj layer) and (NPN) and (PMOS) and (NMOS)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/12/05 18:04
-	11	438/208 and (buried adj layer) and (NPN) and (PMOS) and (NMOS)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/12/05 18:08
-	16	438/309 and (buried adj layer) and (NPN) and (PMOS) and (NMOS)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/12/05 18:10
-	287	257/\$.ccls. and (buried adj layer) and (NPN) and (PMOS) and (NMOS)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/12/05 18:15
-	106	(digital adj circuit) and (analog adj circuit) and (buried adj layer)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/05/06 11:02
-	64	257/\$.ccls. and (digital adj circuit) and (analog adj circuit) and (buried adj layer)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/05/06 11:24
-	25	438/\$.ccls. and (digital adj circuit) and (analog adj circuit) and (buried adj layer)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/05/06 11:25
-	190	438/\$.ccls. and (buried adj layer) and (NPN) and (PMOS) and (NMOS)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/05/06 14:51
-	531	257/508	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/07/28 11:24
-	258	257/509	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/07/28 11:39
-	273	257/503	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/07/28 11:46
-	71	257/504	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/07/28 11:49
-	142	257/505	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/07/28 11:53



